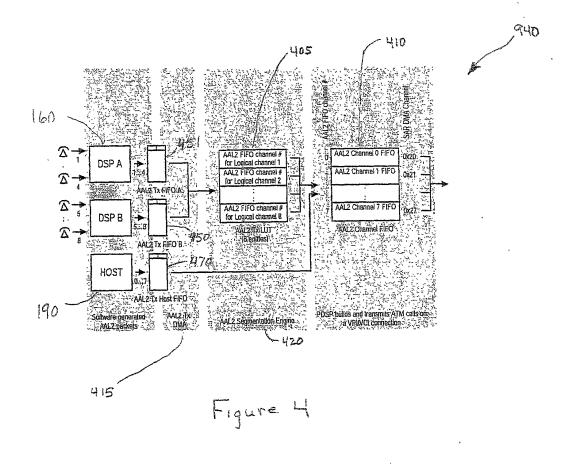
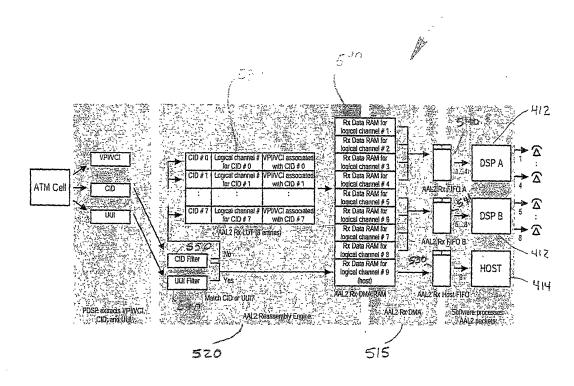
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Figure 2

Figure 3





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Figure 5

56-byte Aligned ATM cell

ATM Header (5 bytes)	
	ATM Payload (Byte 1 thru 47)
Byte 48	Port ID, Priority Level

Figure 7

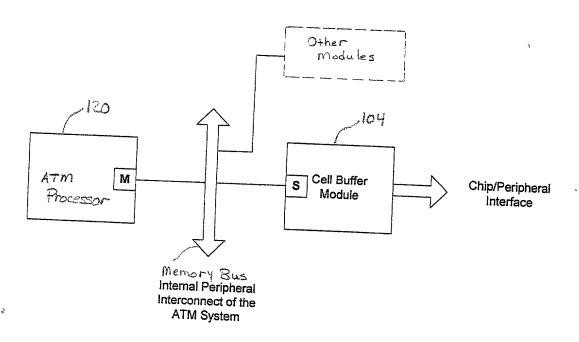


Figure 6 A

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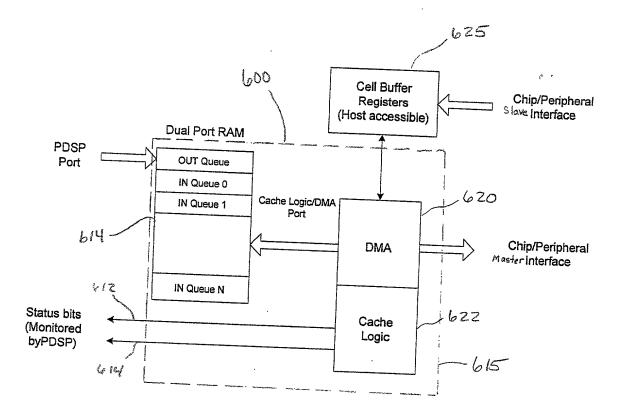


Figure 6B

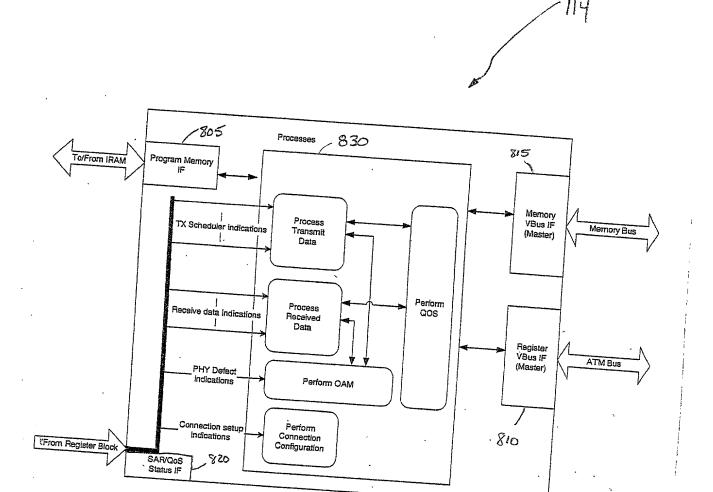


Figure 8

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